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Sheet 1 of 1

SERIAL NO. ATTY. DOCKET NO. FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE 58268-00199 PATENT AND TRADEMARK OFFICE Cont_Appln APPLICANT LIST OF REFERENCES CITED BY APPLICANT Wen-Cheng TSENG et al. FILING DATE (Use several sheets if necessary) GROUP March 22, 2004 2182 **U.S. PATENT DOCUMENTS** EXAMINER SUB-**FILING** DOCUMENT **INITIAL** CLASS DATE DATE NAME **CLASS** NO. ES 5,423,015 AA 06/06/1995 Chung ES AB 5,644,784 07/01/1997 Peek ES AC 5,909,686 06/01/1999 Muller et al. ES AD 6,061,351 05/09/2000 Erimli et al. 5,727,126 03-10-1998 Eichfeld et al. ES ΑE ES 5,794,033 08-11-1998 Aldebert et al. AF ES 6,199,192 09-12-2000 Kao et al. FOREIGN PATENT DOCUMENTS DOCUMENT SUB-**TRANSLATION** NO. **CLASS** NO PART. YES DATE CLASS COUNTRY NO ES AG 99/00936 PCT 01/07/1999 AH ΑJ ΑK AL OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.) AM lan Page, "Reconfigurable Processor Architectures," Microprocessors and Microsystems, IPC Business Press Ltd., London, Great Britain, Vol. 20, No. 3, 1 May 1996, p. 185-196. AN AO EXAMINER **DATE CONSIDERED** /Eron Sorrell/ 01/05/2007

Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not

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*EXAMINER:

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